PATENT APPLICATION

Legal Department, DL429 Intellectual Property Administration P. O. Box 7599 Loveland, Colorado 80537-0599

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Inventor(s) Agmes Óliver Barnes

Serial No.: 09/828083 Examiner: Unknown

Filing Date: Apr 05, 2001

**Group Art Unit:** 

Title:

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[3] - Multi-Purpose Transistor Array

OFFICIAL DRAFTSPERSON **Drawing Processing Branch** Washington, D.C. 20231

## DRAWING TRANSMITTAL LETTER

Enclosed herewith please find:

\_ sheets of redlined drawing(s) which indicate proposed changes to the drawing(s). Upon approval of these proposed changes, formal drawing(s) will be submitted. ( ) \_\_\_\_\_ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) which accompanied the Office Action dated sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) and approved in the Notice of Allowability dated ( ) \_\_\_\_\_ sheets of formal drawing(s). Please substitute these formal drawing(s) for the informal drawing(s) originally filed. sheets of formal drawing)s), as required by the Notice To File Corrected Application Papers dated 05/21/01.

Examiner's approval of the entry of these drawings is respectfully requested. No new matter has been added.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C.

20231.

Date of Deposit: September 12, 20

Typed Name: Patricia Filsinger, PF
Signature: Patricia Filsinger

Respectfully submitted,

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